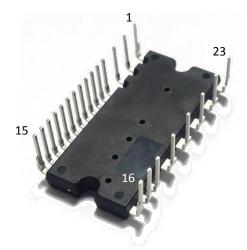


AIM5D05B060N1

Dual-In-Line Package Intelligent Power Module

External View



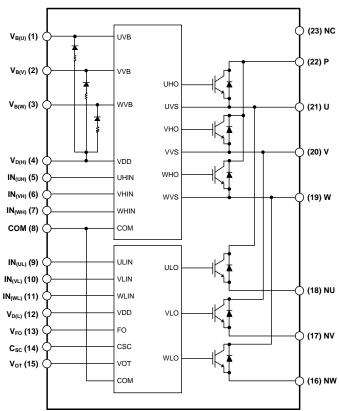
Size: 33.4 x 15 x 3.6 mm

Features and Functions

- UL Recognized: UL1557 File E345245
- 600V-5A (Trench Shielded Planar Gate IGBT)
- 3 phase Inverter module including HVIC drivers
- Built-in bootstrap diodes with integrated current limiting resistor
- Control supply under-voltage lockout protection (UVLO)
- Over-temperature (OT) protection (V_{OT}) pin open
- Temperature monitoring (V_{OT}) 10k Ω resistor connection
- Short-circuit current protection (C_{SC})
- Fault out signal (V_{FO}) corresponding to SC, UV and OT fault
- Wide input interface (3-18V), Schmitt trigger receiver circuit (Active High)
- Isolation ratings of 2000Vrms/min

Applications

 AC 100~240Vrms class low power motor drives like refrigerators, dishwashers, fan motors, washing machines and air-conditioners





Internal Equivalent Circuit



Ordering Information

Р	art Number	Temperature Range	Package	Environmental
AI	M5D05B060N1	150°C	IPM-5	



AOS Green Products use reduced levels of Halogens, and are also RoHS compliant. Please visit www.aosmd.com/media/AOSGreenPolicy.pdf for additional information.

Pin Description

Part Number	Pin Name	Pin Function	
1	V _{B(U)}	High-Side Bias Voltage for U-Phase IGBT Driving	
2	V _{B(V)}	High-Side Bias Voltage for V-Phase IGBT Driving	
3	V _{B(W)}	High-Side Bias Voltage for W-Phase IGBT Driving	
4	V _{D(H)}	High-Side Common Bias Voltage for IC and IGBTs Driving	
5	IN _(UH)	Signal Input for High-Side U-Phase	
6	IN _(VH)	Signal Input for High-Side V-Phase	
7	IN _(WH)	Signal Input for High-Side W-Phase	
8	COM	Common Supply Ground	
9	IN _(UL)	Signal Input for Low-Side U-Phase	
10	IN _(VL)	Signal Input for Low-Side V-Phase	
11	IN _(WL)	Signal Input for Low-Side W-Phase	
12	V _{D(L)}	Low-Side Common Bias Voltage for IC and IGBTs Driving	
13	V _{FO}	Fault Output	
14	C _{SC}	Capacitor (Low-Pass Filter) for Short-circuit Current Detection Input	
15	V _{OT}	Over-Temperature Output	
16	NW	Negative DC-Link Input for W-Phase	
17	NV	Negative DC-Link Input for V-Phase	
18	NU	Negative DC-Link Input for U-Phase	
19	W	Output for W-Phase	
20	V	Output for V-Phase	
21	U	Output for U-Phase	
22	Р	Positive DC-Link Input	
23	NC	No Connection	



Symbol	Parameter	Conditions	Ratings	Units	
Inverter Pa	art				
V _{PN}	Supply voltage	Applied between P - NU,NV,NW	450	V	
V _{PN(surge)}	Supply voltage (surge) Applied between P - NU,NV,NW		500	V	
V _{CES}	Collector-emitter voltage		600	V	
	Output phase ourrept	T _C =25°C, T _J <150°C	5	А	
Ic	Output phase current	T _C =100°C, T _J <150°C	3	А	
±I _{PK}	Output peak phase current	T _c =25°C, less than 1ms pulse width	10	А	
Pc	Collector dissipation	T _c =25°C, per 1 chip	18.9	W	
ΤJ	Operating junction temperature		-40~+150	°C	
Control (P	rotection) Part				
VD	Control supply voltage Applied between V _{D(H)} -COM, V _{D(L)} -COM		25	V	
V _{DB}	High-side control bias voltage	Applied between V _{B(U)} -U, V _{B(V)} -V, V _{B(W)} -W	25	V	
V _{IN}	Input voltage	Applied between IN _(UH) , IN _(VH) , IN _(WH) , IN _(UL) , IN _(VL) , IN _(WL) - COM		V	
V _{FO}	Fault output supply voltage	Applied between V _{FO} - COM	-0.5~V _D +0.5	V	
I _{FO}	Fault output current	Sink current at V _{FO} terminal	1	mA	
V _{SC}	Current sensing input voltage	Applied between C _{SC} - COM	-0.5~5+0.5	V	
V _{OT}	Temperature output	Applied between Vor - COM	-0.5~5+0.5	V	
Total Syste	• • •				
V _{PN(PROT)}	Self protection supply voltage limit (Short-circuit protection capability)	V _D =13.5~16.5V, Inverter part T _J =150°C, Non-repetitive, less than 2µs	400	V	
Tc	Module case operation temperature	Measurement point of T _C is provided in Figure 1	-30~+125	°C	
T _{STG}	Storage temperature	-	-40~+150	°C	
V _{ISO}	Isolation voltage	60Hz sinusoidal AC 1min between			

Absolute Maximum Ratings (T_J=25°C, Unless Otherwise Specified)

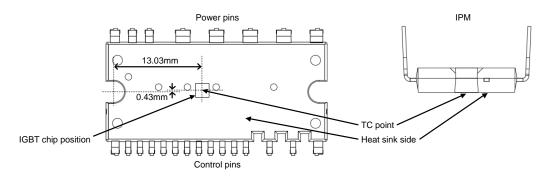


Figure 1. T_c Measurement Point.

Thermal Resistance

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
R _{th(j-c)Q}	Junction to case thermal resistance	Inverter IGBT part (per 1/6 module)	-	-	6.6	K/W
R _{th(j-c)F}	(Note 1)	Inverter FWD part (per 1/6 module)	-	-	8.5	K/W

Note:

1. For the measurement point of case temperature(T_C), please refer to figure 1.



Symbol	Parameter	Conditions			Min.	Тур.	Max.	Units
Inverter Pa	rt							•
	Collector-emitter	$V_D = V_{DB} = 15V$,	I _C =2.5A,	T _{J=} 25°C	-	1.45	1.85	V
$V_{CE(SAT)}$	saturation voltage	V _{IN} =5V I _C =2.5A, T _J =125°C		-	1.65	-	V	
VF	FWD forward voltage	V _{IN} =0	I _F =2.5A,	TJ=25°C	-	1.75	2.15	V
t _{ON}				0.40	0.80	1.40	μs	
t _{C(ON)}]	V _{PN} =300V, V _D =V _{DB} =15V			-	0.10	0.40	μs
t _{OFF}	Switching times	I _C =2.5A, T _J =25°C, ∨	$_{\rm IN}=0V\leftrightarrow 5V$	/	-	0.80	1.40	μs
t _{C(OFF)}]	Inductive load (high-			-	0.08	0.20	μs
t _{rr}					-	0.18	-	μs
1	Collector-emitter leakage		TJ=25°C	;	-	-	1	mA
I _{CES}	current	V _{CE} =V _{CES}	T _J =125°	С	-	-	10	mA
Control (Pr	rotection) Part							
I _{QDH}	Quiescent V _D supply	V _{D(H)} =15V, IN _{(UH, VH,}	wн)=0V	V _{D(H)} - COM	-	-	0.1	mA
I _{QDL}	current	V _{D(L)} =15V, IN _{(UL, VL, W}		V _{D(L)} - COM	-	-	2.1	mA
I _{QDB}	Quiescent V _{DB} supply current	V _{DB} =15V, IN _(UH, VH, WH) =0V V _{B(U)} -U, V _{B(V)} - V, V _{B(W)} - W		-	-	0.3	mA	
V _{SC(ref)}	Short-circuit trip level	V _D =15V (Note 2)		0.455	0.480	0.505	V	
UV _{DT}		Trip level			9.0	10.0	11.0	V
UV_{DR}	Supply circuit under-	Reset level			10.0	11.0	12.0	V
UV _{DBT}	voltage protection	Trip level			8.5	9.5	10.5	V
UV_{DBR}		Reset level			9.5	10.5	11.5	V
Vot	Temperature output	Pull down R=10kΩ LVIC temperature=90°C		2.74	2.92	3.10	V	
•			te 3) LVIC temperature=25°C		1.06	1.25	1.43	V
OTT	Over-temperature	$V_D=15V$, detect	Trip level		100	120	140	°C
OT _{HYS}	protection (Note 4)			s of trip reset	-	10	-	°C
V _{FOH}	Fault output voltage	V _{SC} =0V, V _{FO} circuit:			4.9	-	-	V
V _{FOL}		$V_{SC}=1V$, V_{FO} circuit: $10k\Omega$ to 5V pull-up			-	-	0.5	V
t _{FO}	Fault output pulse width			(Note 5)	20	-	-	μs
I _{IN}	Input current	V _{IN} =5V			-	1.0	-	mA
V _{th(on)}	ON threshold voltage					2.3	2.6	V
V _{th(off)}	OFF threshold voltage	Applied between IN($_{\text{UH}),}$ IN $_{(\text{VH}),}$ II	N _{(WH),} IN _{(UL),}	0.8	1.2		V
$V_{th(hys)}$	ON/OFF threshold hysteresis voltage	$IN_{(VL)}$, $IN_{(WL)} - COM$			-	1.1	-	V
V _{F(BSD)}	Bootstrap diode forward voltage	I _F =10mA including vertices in the second s	oltage drop	by limiting (Note 6)	1.0	1.5	2.0	V
R _{BSD}	Built-in limiting resistance	Included in bootstrag	o diode	· · · · ·	80	100	120	Ω

Electrical Characteristics (T_J=25°C, Unless Otherwise Specified)

Note:

2. Short-circuit protection works only for low-sides.

The IPM does not shutdown IGBTs and output fault signal automatically when temperature rises excessively. When temperature exceeds the
protective level that the user defined, the controller (MCU) should stop the IPM. Temperature of LVIC vs. V_{oT} output characteristics is
described in Figure 3.

4. When the LVIC temperature exceeds OT Trip temperature level (OT_T), OT protection works and fault outputs.

Fault signal F_o outputs when SC, UV or OT protection works. F_o pulse width is different for each protection mode. At SC failure, F_o pulse width is a fixed width (minimum 20µs), but at UV or OT failure, F_o outputs continuously until recovering from UV or OT state. (But minimum F_o pulse width is 20µs).

6. The characteristics of bootstrap diode are described in Figure 2.

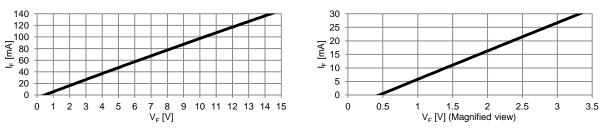


Figure 2. Built-in Bootstrap Diode V_F-I_F Characteristic (@Ta=25°C).

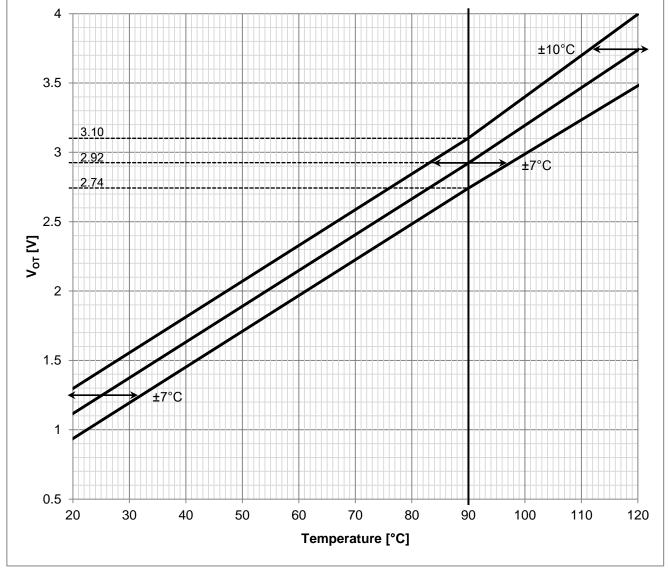
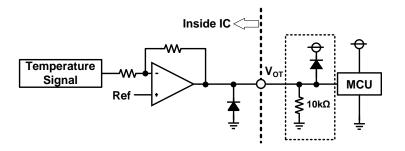


Figure 3. Temperature of LVIC vs. V_{oT} Output Characteristics.



- (1) Connect 10-k to V_{OT} pin if temperature monitoring function is used and leave the V_{OT} pin open (no connect) if not using temperature monitoring and use internal over-temperature shutdown function.
- (2) In the case of using V_{OT} with low voltage controller like 3.3V MCU, V_{OT} output might exceed control supply voltage 3.3V when temperature rises excessively. If system uses low voltage controller, it is recommended to insert a clamp diode between control supply of the controller and V_{OT} output for preventing over voltage destruction.

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Figure 4. V_{ot} Output Circuit.

Mechanical Characteristics and Ratings

Parameter	Conditions		Тур.	Max.	Units
Mounting torque	Mounting screw: M3 (Note 7) Recommended 0.69N m	0.59	0.69	0.78	Nm
Weight		-	5.25	-	g
Flatness	Refer to Figure 5	-50	-	100	μm

Note:

7. Plain washers (ISO 7089~7094) are recommended.

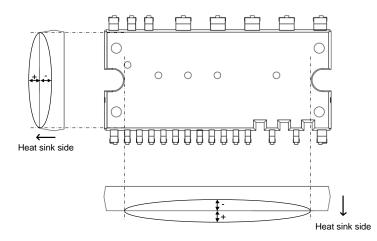


Figure 5. Flatness Measurement Position.

Recommended Operation Conditions

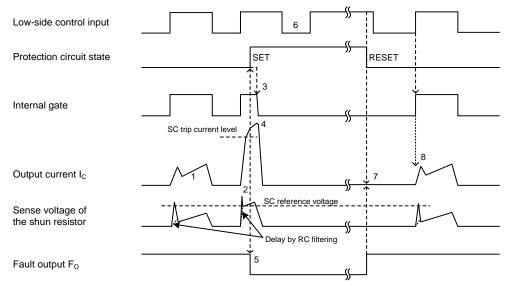
Symbol	Parameter	Conditions		Тур.	Max.	Units
V _{PN}	Supply voltage	Applied between P-NU, NV, NW	0	300	400	V
VD	Control supply voltage	Applied between $V_{D(H)} - COM$, $V_{D(L)} - COM$	13.5	15.0	16.5	V
V _{DB}	High-side bias voltage	Applied between V _{B(U)} -U, V _{B(V)} -V, V _{B(W)} -W	13.5	15.0	18.5	V
dV _D /dt, dV _{DB} /dt	Control supply variation		-1	-	1	V/µs
t _{dead}	Arm shoot-through blocking time	For each input signal	1.0	-	-	μs
f _{PWM}	PWM input frequency	-40°C < T _J < 150°C	-	-	20	kHz
Pwin(on)	Minimum input pulse	(Note 8)	0.4	-	-	μs
Pwin(OFF)	width	(Note 8)	0.4	-	-	μs
COM	COM variation	Between COM - NU, NV, NW (including surge)	-5.0	-	+5.0	V

Note:

8. IPM might not make response if the input signal pulse width is less than P_{WIN(ON)}, P_{WIN(OFF)}.

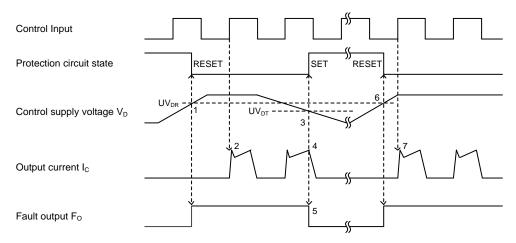


Time Charts of the IPM Protective Function



- (1) Normal operation: IGBT turns on and outputs current.
- (2) Short-circuit current detection (SC trigger).
- (3) All low-side IGBT's gates are hard interrupted.
- (4) All low-side IGBTs turn OFF.
- (5) F_0 outputs for t_{FO} =minimum 20µs.
- (6) Input = "L" : IGBT OFF.
- (7) Fault output finish, but output current will not turn on until next ON signal (L \rightarrow H).
- (8) Normal operation: IGBT turns on and outputs current.

Figure 6. Short-Circuit Protection (Low-side Operation only with the external shunt resistor and RC filter).



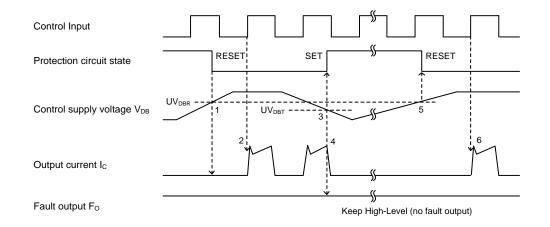
- (1) Control supply voltage V_D exceeds under voltage reset level (UV_{DR}), but IGBT turns on by next ON signal (L \rightarrow H).
- (2) Normal operation: IGBT turns on and outputs current.
- (3) V_D level drops to under voltage trip level (UV_{DT}).
- (4) All low-side IGBTs turn OFF in spite of control input condition.
- (5) F_0 output for t_{F0}=minimum 20µs, but output is extended during V_D keeps below UV_{DR}.

(6) V_D level reaches UV_{DR} .

(7) Normal operation: IGBT turns on and outputs current.

Figure 7. Under-Voltage Protection (Low-side, UV_D).





(1) Control supply voltage V_{DB} rises. After the voltage reaches under voltage reset level UV_{DBR}, IGBT turns on by next ON signal (L \rightarrow H).

(2) Normal operation: IGBT turns on and outputs current.

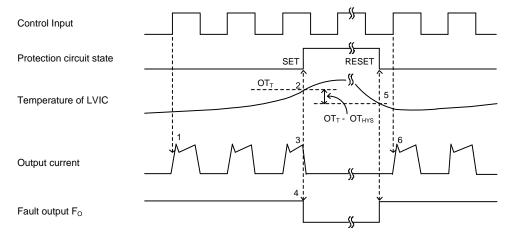
(3) V_{DB} level drops to under voltage trip level (UV_{DBT}).

(4) All high-side IGBTs turn OFF in spite of control input condition, but there is no FO signal output.

(5) V_{DB} level reaches V_{DBR}.

(6) Normal operation: IGBT turns on and outputs current.





(1) Normal operation: IGBT turns on and outputs current.

(2) LVIC temperature exceeds over-temperature trip level (OT_T).

(3) All low-side IGBTs turn OFF in spite of control input condition.

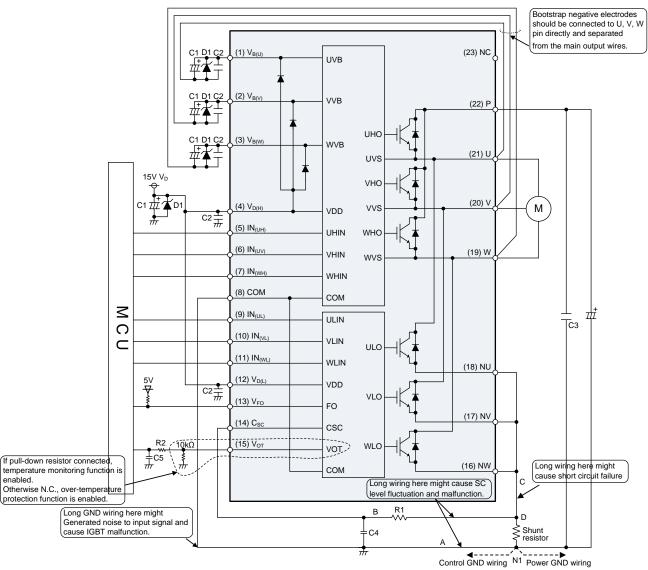
(4) F_O outputs for t_{FO}=minimum 20µs, but output is extended during LVIC temperature keeps over OT_T.

(5) LVIC temperature drops to over-temperature reset level.

(6) Normal operation: IGBT turns on by the next ON signal ($L \rightarrow H$).

Figure 9. Over-Temperature Protection (Low-side, Detecting LVIC temperature).



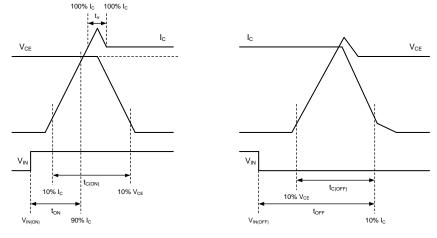


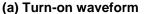
Example of Application Circuit

- (1) If control GND is connected with power GND by common broad pattern, it may cause malfunction by power GND fluctuation. It is recommended to control GND and power GND at only a point N1 (near the terminal of shunt resistor).
- (2) It is recommended to insert zener diode D1 (24V/1W) between each pair of control supply pins to prevent surge destruction.
- (3) To prevent surge destruction, the wiring between the smoothing capacitor and the P, N1 terminals should be as short as possible. Generally a 0.1~0.22µF snubber capacitor C3 between the P-N1 terminals is recommended.
- (4) R1, C4 of RC filter for preventing protection circuit malfunction is recommended to select tight tolerance, temp-compensated type. The time constant R1C4 should be set so that SC current is shut down within 2µs. (1.5µs~2µs is general value). SC interrupting time might vary with the wiring pattern, so the enough evaluation on the real system is necessary.
- (5) R2, C5 of RC filter for temperature monitoring is recommended to select tight tolerance, temp-compensated type. The time constant R2C5 should be set so that VOT has noise immunity. Recommended value of R2 and C5 are 2kΩ and 10nF (10 to 30µs range of time constant).
- (6) To prevent malfunction, the wiring of A, B, C should be as short as possible.
- (7) The point D at which the wiring to CSC filter is divided should be near the terminal of shunt resistor. NU, NV, NW pins should be connected at near NU, NV, NW pins.
- (8) All capacitors should be mounted as close to the terminals as possible. (C1: good temperature, frequency characteristic electrolytic type and C2: 0.22µ~2µF, good temperature, frequency and DC bias characteristic ceramic type are recommended).
- (9) Input drive is high-active type. There is a minimum 3.5kΩ pull-down resistor in the input circuit of IC. To prevent malfunction, the wiring of each input should be as short as possible. When using RC coupling circuit, make sure the input signal level meet the turn-on and turn-off threshold voltage.



- (10) V_{FO} output is open drain type. It should be pulled up to MCU or control power supply (e.g. 5V, 15V) by a resistor that makes I_{FO} up to 1mA. (I_{FO} is estimated roughly by the formula of control power supply voltage divided by pull-up resistor. In the case of pulled up to 5V, 10kΩ (over 5kΩ) is recommended).
- (11) Thanks to build-in HVIC, direct coupling to MCU without any opto-coupler or transformer isolation is possible.
- (12) If high frequency noise superimposed to the control supply line, IC malfunction might happen and cause IPM erroneous operation. To avoid such problem, line ripple voltage should meet dV/dt ≤ ±1V/µs, Vripple ≤ 2Vp-p.
- (13) For IPM, it isn't recommended to drive same load by parallel connection with other phase IGBT or other IPM.





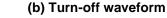
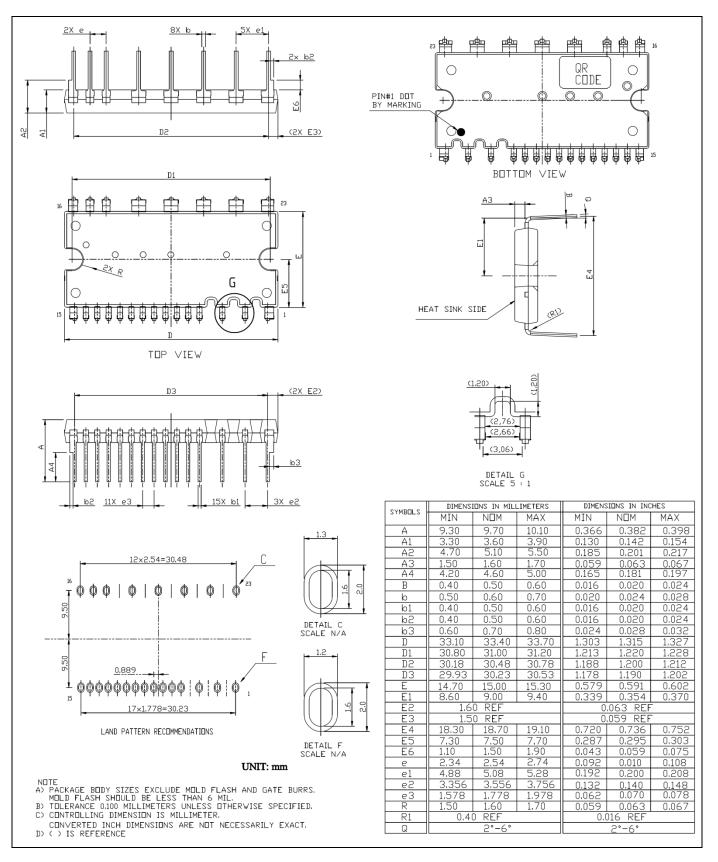


Figure 10. Switching Times Definition.



Package Dimensions





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