

General Description

AOZ1377DI is a current-limiting protection switch targeting applications that require comprehensive protections. Both VIN and VOUT terminals are rated at 28V absolute maximum.

The AOZ1377DI is a USB-C protection switch with a 5A/20VIN rating, capable of operating as either a sink or a source switch. There is a programmable soft-start feature that controls the inrush current for highly capacitive loads. The supply current reduces to 3μA in shut-down. It also has Input Under-Voltage Lock Out (UVLO), Input Over-Voltage Protection (OVP), and Thermal Shut Down Protection (TSD).

AOZ1377DI has True Reverse Current Blocking (TRCB) protection to avoid undesired reverse-current from VOUT to VIN. It also features an internal current-limiting circuit that protects the supply from large load current. The current limit threshold can be set externally with a resistor.

AOZ1377DI-01 will auto-restart when the fault conditions are cleared. AOZ1377DI-02 will latch the power switch off and EN must be reset to restart the device.

AOZ1377DI is available in small 3mm x 3mm 10-pin DFN package.

Features

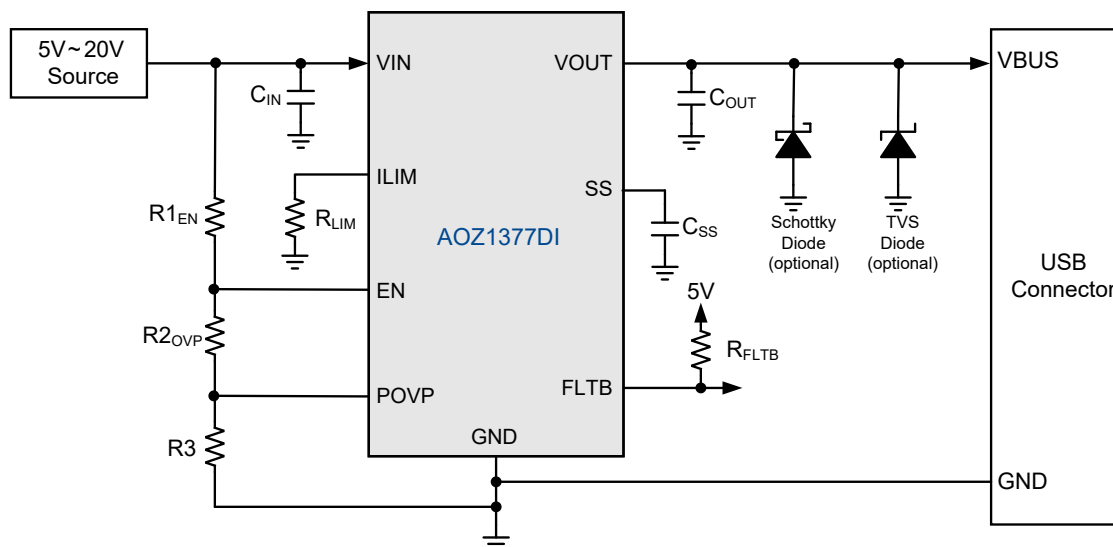
- 28V abs max voltage rating on VIN and VOUT pin
- Typical R_{ON} : 19mΩ
- Support up to 7A at operating max 23V
- Programmable current limit
- True Reverse-Current Blocking (TRCB)
- Input Over-Voltage Protection (OVP)
- Short-Circuit Protection (SCP)
- Input Under-Voltage Lock Out (UVLO)
- Thermal Shut Down Protection (TSD)
- HBM ESD rating: ±4kV on all pins
- CDM ESD rating: ±1kV
- IEC 61000-4-2: ±8kV on VOUT and VIN
- IEC 61000-4-5: ±40V on VOUT, No cap
- IEC 62368-1:2018 (3rd Edition)
- UL2367 recognition

Applications

- USB PD power source switch
- Smartphones and tablets
- Notebooks, ultra-books and desktops
- Portable devices



Typical Application



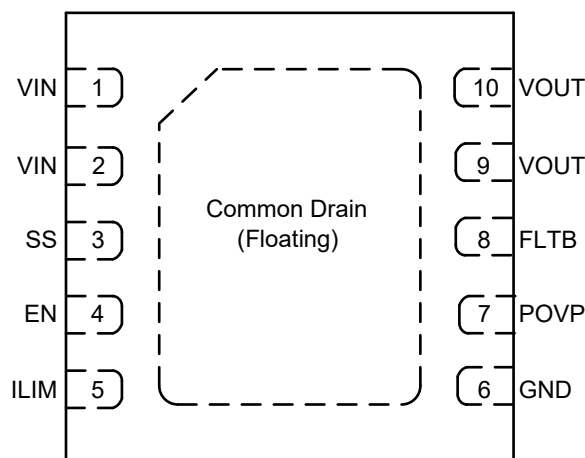
Ordering Information

Part Number	Fault Recovery	Operating Voltage Range	Package	Environmental
AOZ1377DI-01	Auto-Restart	3.4V - 23V	DFN3x3-10L	RoHS
AOZ1377DI-02	Latch Off	3.4V - 23V	DFN3x3-10L	RoHS



AOS products are offered in packages with Pb-free plating and compliant to RoHS standards. Please visit <https://aosmd.com/sites/default/files/media/AOSGreenPolicy.pdf> for additional information.

Pin Configuration



DFN3x3-10L
(Top Transparent View)

Pin Description

Pin Number	Pin Name	Pin Function
1, 2	VIN	Supply input. Connected to internal power regulator. They are internally connected together.
3	SS	Soft Start control. Connect a capacitor CSS from SS to GND to set the soft start time.
4	EN	Enable input. Active high.
5	ILIM	Current limit set pin. Connect a 1% resistor RLIM from ILIM to GND to set the current limit threshold.
6	GND	Ground
7	POVP	Programmable Over Voltage Protection. Use resistor divider from VIN to GND to set the OVP threshold.
8	FLTB	Open-drain Fault indicator.
9, 10	VOUT	Power output. Connect to Type C port. They are internally connected together.
EXP	EXP	Exposed Thermal Pad. It is the common drain node for the power switches and it must be electrically isolated. Solder to a metal surface directly underneath the EXP and connect to floating copper thermal pads on multiple PCB layers through many VIAs. For best thermal performance, make the floating copper pads as large as possible.

Absolute Maximum Ratings

Exceeding the Absolute Maximum ratings may damage the device.

Parameter	Rating
VIN, VOUT to GND	-0.3V to +28V
EN, ILIM, SS, FLT_B, POVP to GND	-0.3V to +6V
Junction Temperature (T _J)	+150°C
Storage Temperature (T _S)	-65°C to +150°C
ESD Rating HBM All Pins	±4kV
IEC 61000-4-2: VOUT and VIN	±8kV

Recommended Operating Conditions

The device is not guaranteed to operate beyond the Maximum Recommended Operating Conditions.

Parameter	Rating
VIN, VOUT to GND	3.4V to 23V
EN, ILIM, SS, FLT_B to GND	0V to 5.5V
POVP to GND	0V to 3V
Switch DC Current (I _{SW})	0A to 7A
Ambient Temperature (T _A)	-40°C to +85°C

Electrical Characteristics

T_A = 25°C, VIN = 12V, EN = 5V, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
General						
V _{VIN}	Input Supply Voltage		3.4		23	V
V _{UVLO_R}	Under-voltage Lockout Threshold	VIN rising	2.9		3.35	V
V _{UVLO_HYS}	Under-voltage Lockout Hysteresis	VIN falling		250		mV
I _{VIN_ON}	Input Quiescent Current	I _{OUT} = 0 A		900		uA
I _{VIN_OFF}	Input Shutdown Current	EN = 0 V		3		μA
R _{ON_20V}	Switch ON-Resistance	VIN = 20V, I _{OUT} = 1 A		19		mΩ
R _{ON_5V}		VIN = 5V, I _{OUT} = 1 A		19		mΩ
V _{EN_H}	Enable Input Logic High Threshold	EN rising	1.05	1.20	1.35	V
V _{EN_L}	Enable Input Logic Low Threshold	EN falling	0.85	1.00	1.15	V
I _{EN_BIAS}	Enable Input Bias Current	EN = 1.8V		20		nA
V _{FLT_B_LO}	FLT_B Pull-down Voltage	I _{SINK} = 3mA			0.3	V
Dynamic Characteristics						
t _{D_ON}	Turn-On Delay Time (From EN 50% to VOUT=0.5V)	VIN = 12V, C _{SS} = 22nF R _{LIM} = 3.92kΩ		1		ms
t _{ON}	Turn-On Time (VOUT from 10% to 90% of 12V VIN)	VIN = 12V, C _{SS} = 22nF R _{LIM} = 3.92kΩ		3.2		ms
t _{REC}	FLT_B Recovery Time	Fault is removed (AOZ1377DI-01 Only)		130		ms
True Reverse-Current Blocking (TRCB)						
V _{T_TRCB}	TRCB Protection Trip Point	VOUT-VIN, VOUT rising		30		mV
t _{TRCB}	TRCB Response Time	VOUT-VIN > V _{T_TRCB} + 500mV		1		μs

Electrical Characteristics

$T_A = 25^\circ\text{C}$, $V_{IN} = 12\text{V}$, $EN = 5\text{V}$, unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Input Over-Voltage Protection (OVP)						
V_{POVP}	Over-voltage Lockout Threshold	VPOVP rising		1.2		V
V_{POVP_HYS}	Over-voltage Lockout Hysteresis	AOZ1377DI-01 only		100		mV
I_{POVP_BIAS}	POVP Input Bias Current	POVP = 1.8V		20		nA
t_{DELAY_OVP}	OVP Turn-Off Delay			1		μs
Over Current Protection (OCP)						
I_{LIM}	Current Limit Threshold	$R_{LIM} = 2.74\text{k}\Omega$ (1% Tolerance)	6.4	7.1	7.8	A
		$R_{LIM} = 3.92\text{k}\Omega$ (1% Tolerance)	4.5	5.0	5.5	
		$R_{LIM} = 6.65\text{k}\Omega$ (1% Tolerance)	2.55	3.0	3.45	
t_{OCP}	Over-Current Duration before Switch Off	From $I_{OUT} \geq I_{LIM}$ to Switch Off		1		ms
t_{OCP_FLTB}	Over-Current Flag Delay	From $I_{OUT} \geq I_{LIM}$ to FLTB pulled low		1		ms
Short-Circuit Protection (SCP)						
$t_{SC_RESPONSE}$	Response time to open the switch after a VOUT short circuit condition is detected	$V_{IN} - V_{OUT} > 500\text{mV}$		1		μs
Thermal Shutdown (TSD)						
T_{SD}	Thermal Shutdown Threshold	Temperature rising		140		$^\circ\text{C}$
T_{SD_HYS}	Thermal Shutdown Hysteresis	Temperature falling (AOZ1377DI-01 only)		30		$^\circ\text{C}$

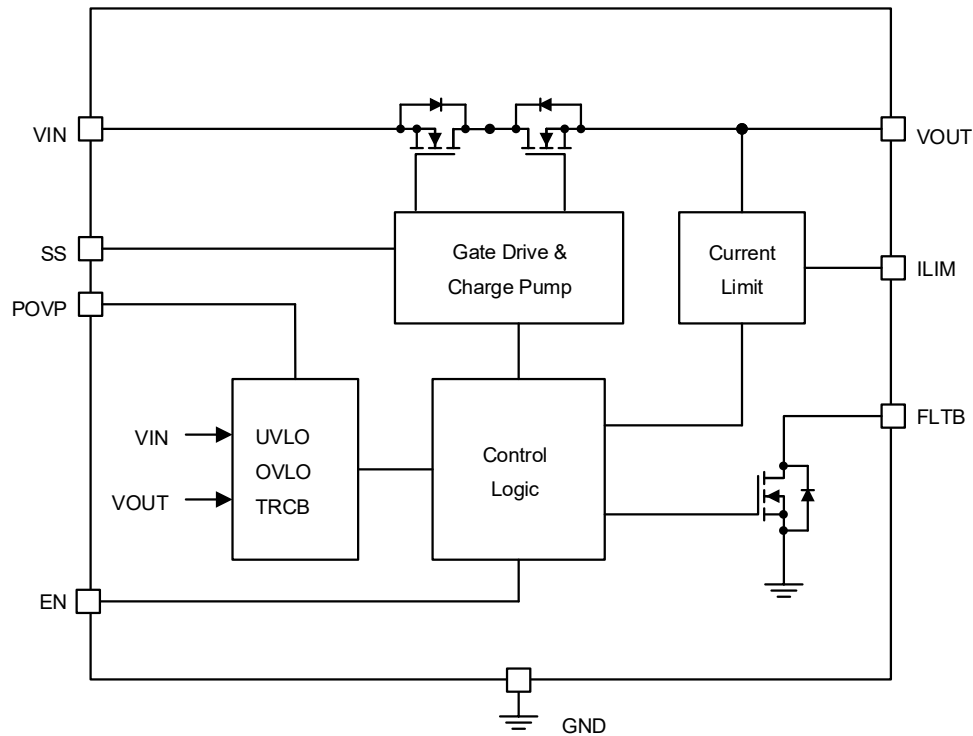
Thermal Characteristics

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$R_{th(J-C)}$	Thermal Resistance from junction to case ⁽¹⁾			7		$^\circ\text{C/W}$
$R_{th(J-A)}$	Thermal Resistance from junction to ambient ⁽¹⁾			35		$^\circ\text{C/W}$

Note:

1. Thermal resistance measured on AIS 4layer EVS.

Functional Block Diagram



Timing Diagrams

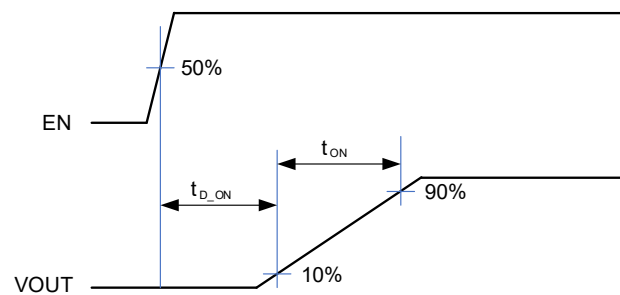


Figure 1. Power On Delay and Soft Start Time

Timing Diagrams (Cont.)

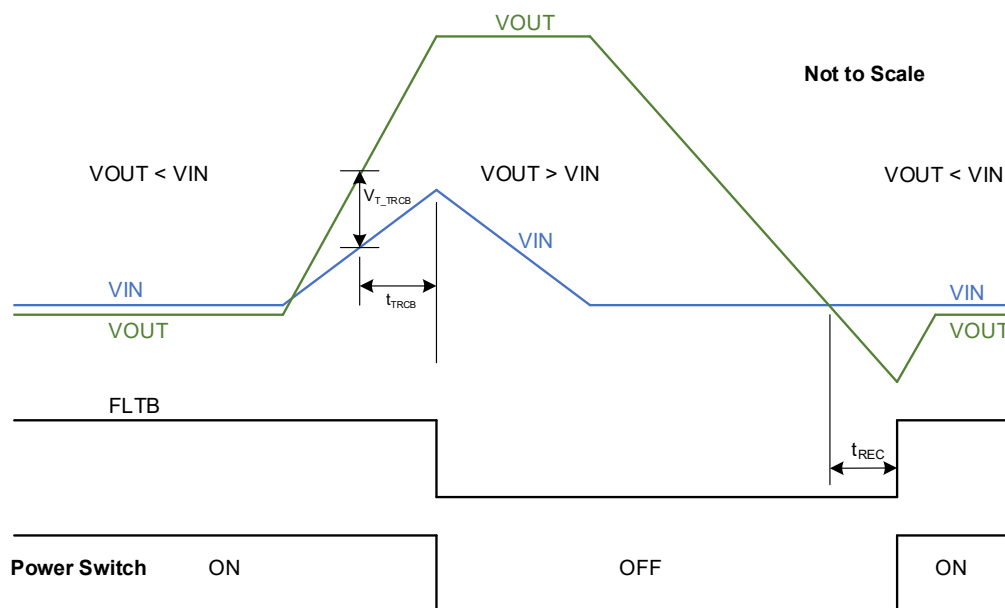


Figure 2. True Reverse Current Blocking (TRCB) Operation (AOZ1377DI-01)

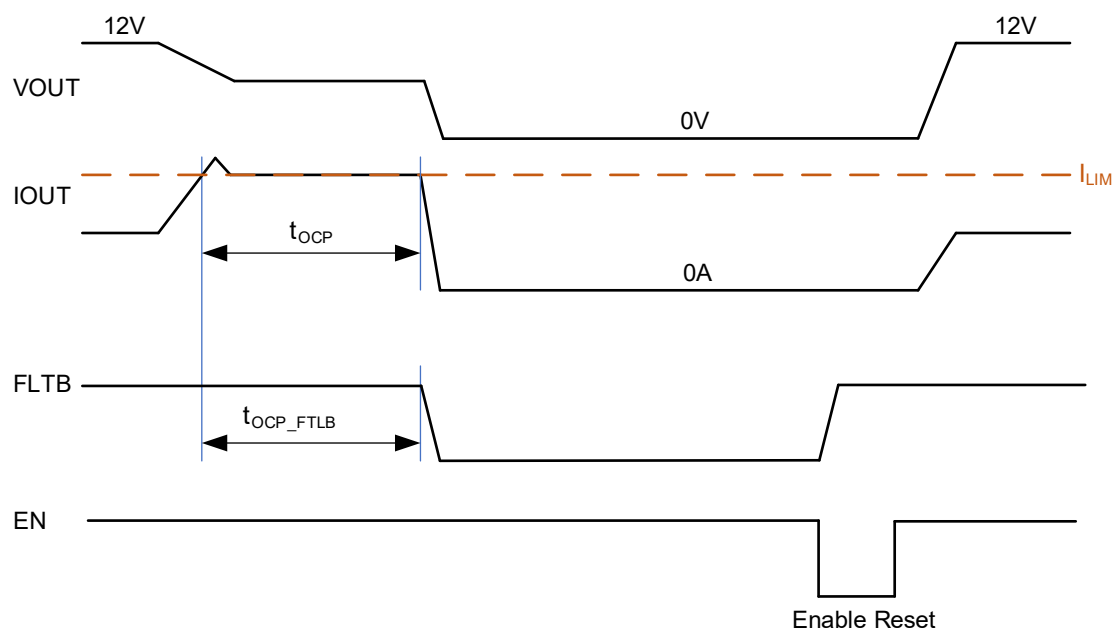
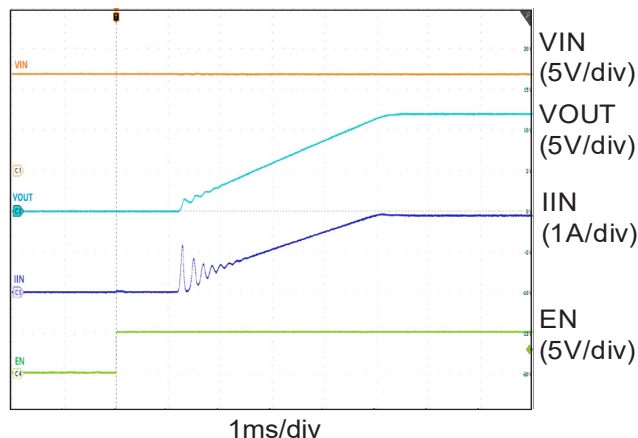


Figure 3. Over Current Protection (OCP) Operation for Latch Off Version. AOZ1377DI-02. $V_{IN} = 12V$.

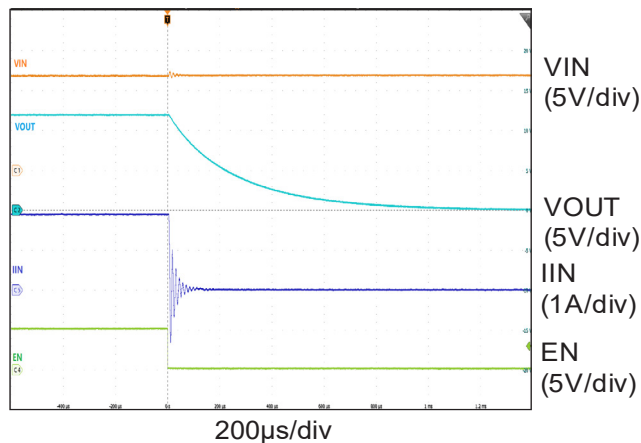
Typical Characteristics

$T_A = 25^\circ\text{C}$, $V_{IN} = 12\text{V}$, $C_{IN} = 10\mu\text{F}$, $C_{OUT} = 40\mu\text{F}$, $C_{SS} = 22\text{nF}$, $C_{CAP} = 1\text{nF}$, $R_{LIM} = 3.92\text{k}\Omega$ unless otherwise specified.

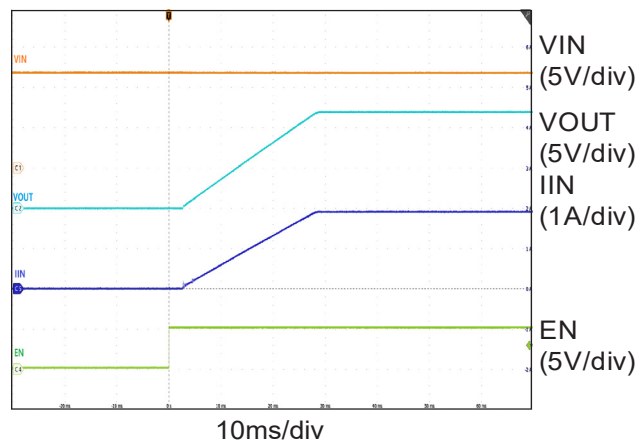
**Soft Start by EN
(LOAD = 6.5Ω)**



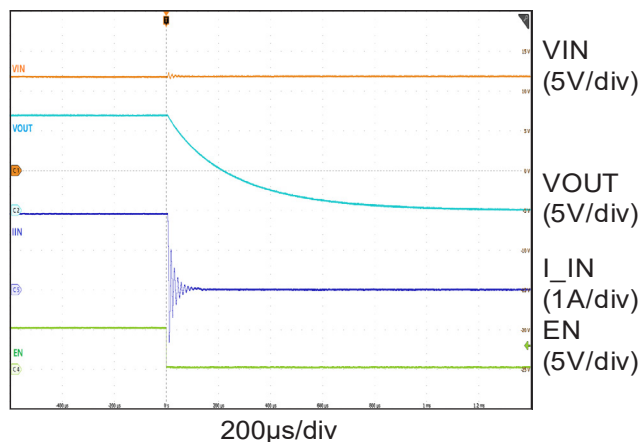
**Shutdown by EN
(LOAD = 6.5Ω)**



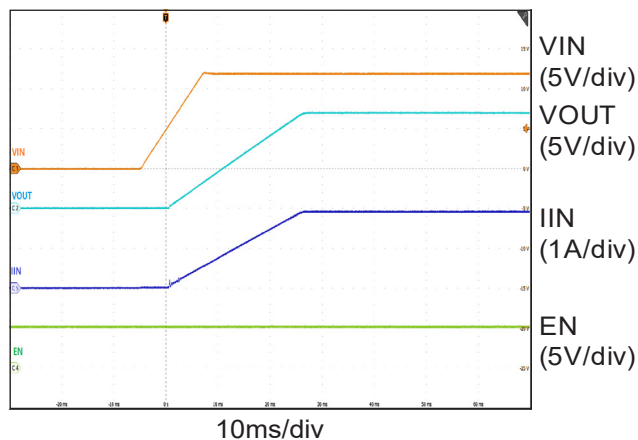
**Soft Start by EN
(LOAD = 6.5Ω , $C_{SS} = 134\text{nF}$)**



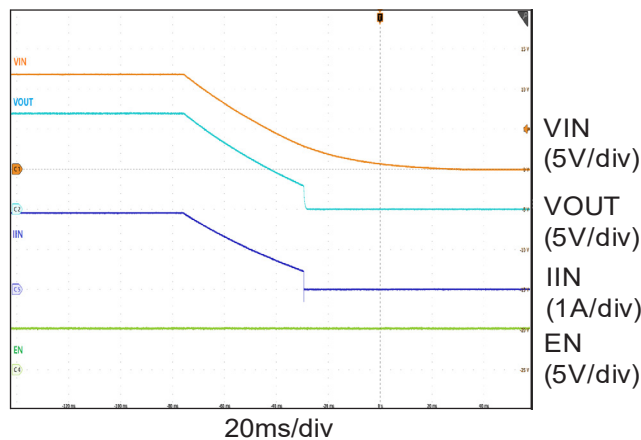
**Shutdown by EN
(LOAD = 6.5Ω , $C_{SS} = 134\text{nF}$)**



**Soft Start by VIN
(LOAD = 6.5Ω , $C_{SS} = 134\text{nF}$)**



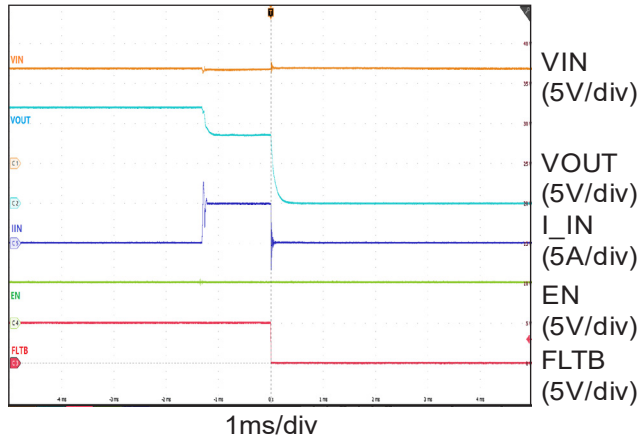
**Shut Down by VIN
(LOAD = 6.5Ω , $C_{SS} = 134\text{nF}$)**



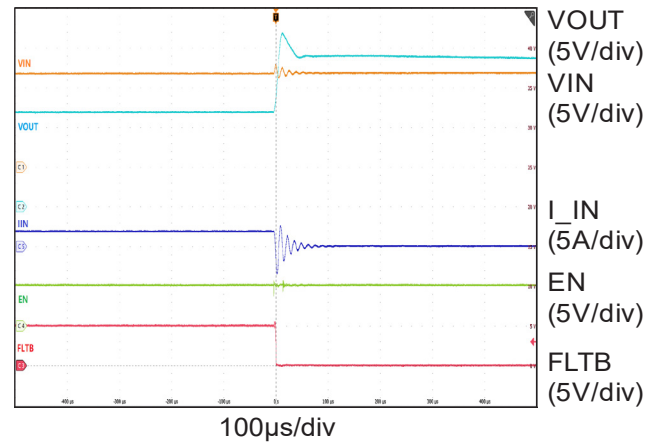
Typical Characteristics

$T_A = 25^\circ\text{C}$, $V_{IN} = 12\text{V}$, $C_{IN} = 10\mu\text{F}$, $C_{OUT} = 40\mu\text{F}$, $C_{SS} = 22\text{nF}$, $C_{CAP} = 1\text{nF}$, $R_{LIM} = 3.92\text{k}\Omega$ unless otherwise specified.

**Over Current Protection
(LOAD = 2Ω Plugged In)**

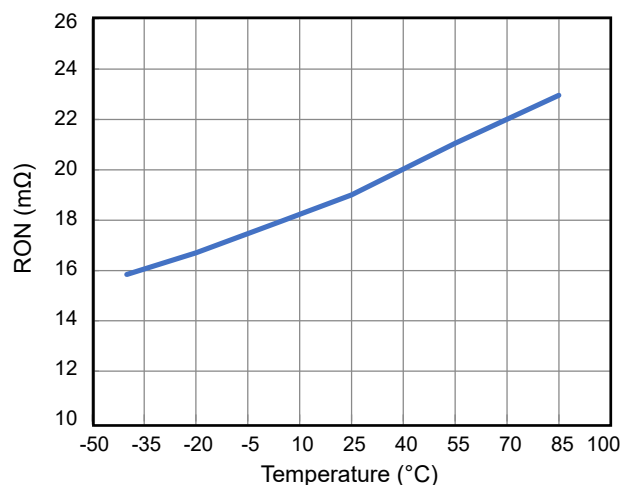


**Reverse Current Blocking
(LOAD = 6.5Ω , VOUT pulled up to 20V)**

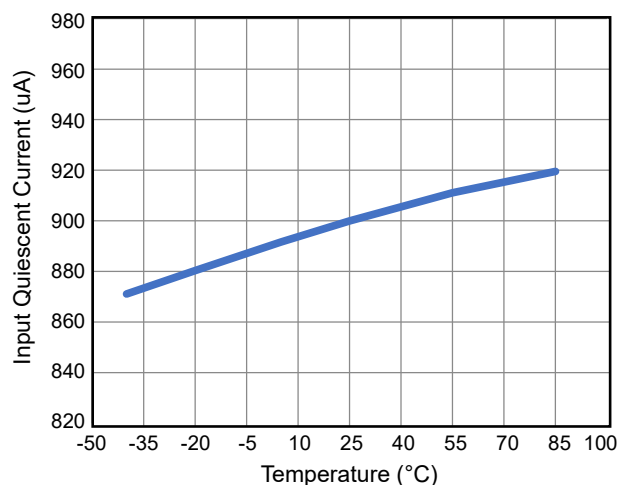


Typical Characteristics

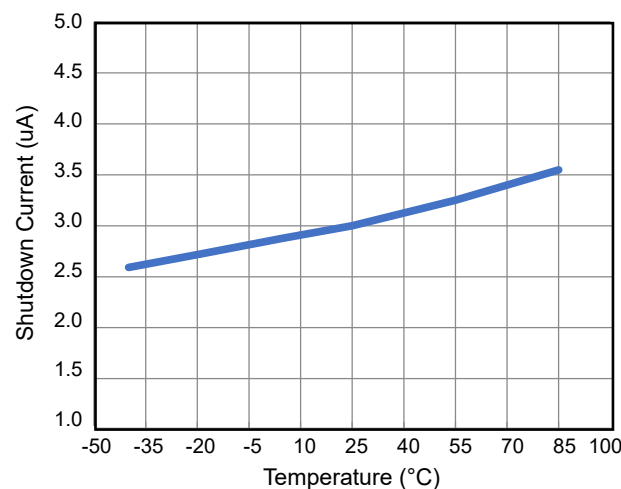
$T_A = 25^\circ\text{C}$, unless otherwise specified.



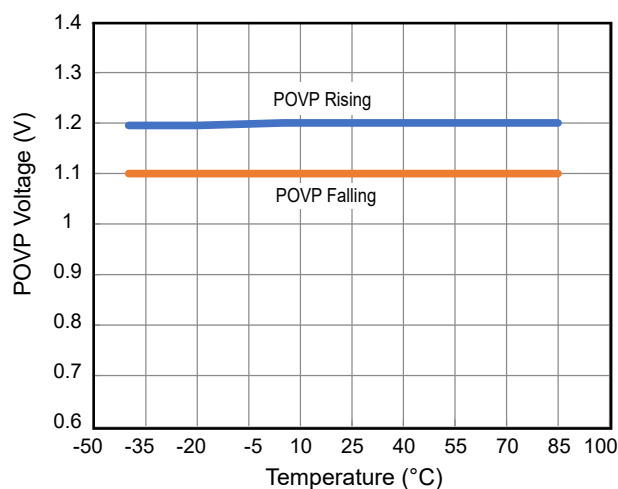
On Resistance vs. Temperature
(VIN = 20V)



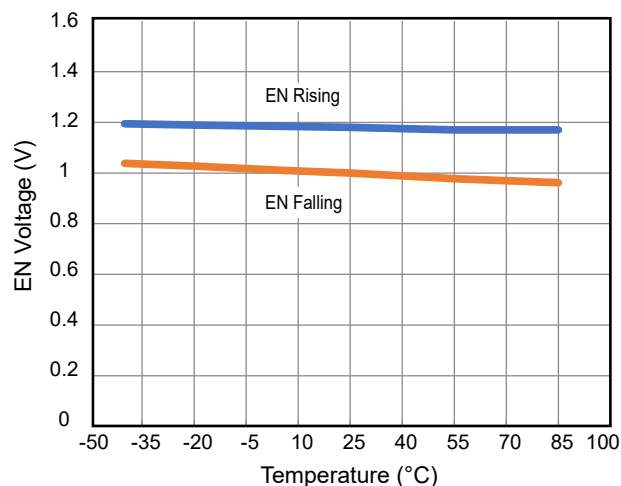
Quiescent Current vs. Temperature
(VIN = 20V)



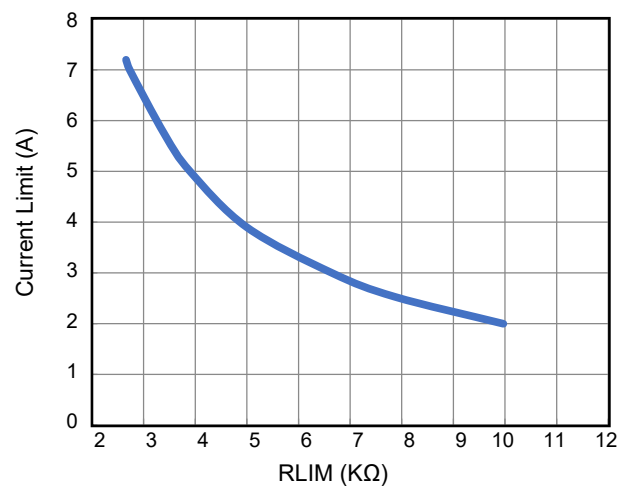
Shutdown Current vs. Temperature
(VIN = 20V)



POVP Pin Threshold vs. Temperature



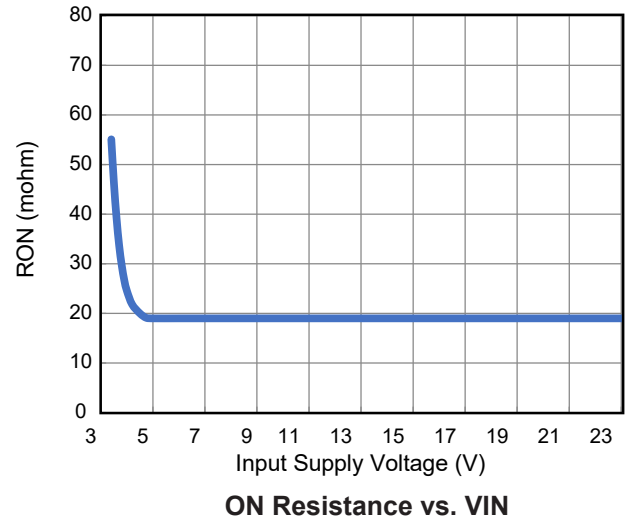
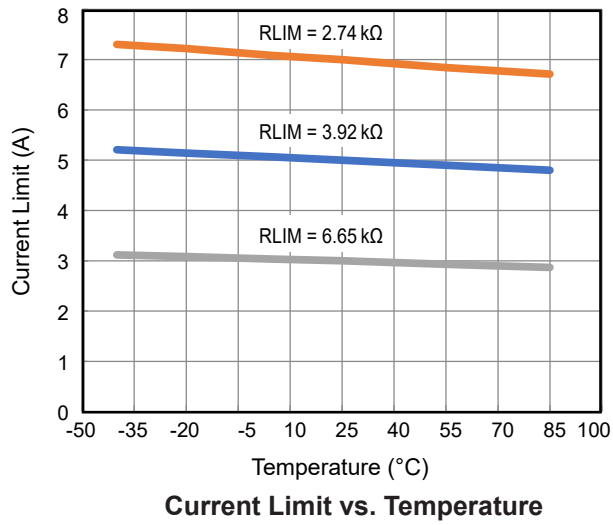
EN Threshold vs. Temperature



Current Limit vs. RLIM

Typical Characteristics

$T_A = 25^\circ\text{C}$, unless otherwise specified.



Functional Description

The AOZ1377 is a power switch with current limiting, offering over-voltage, over-current, SCP, reverse-current, and thermal shutdown protections.

The device has true reverse-current blocking (TRCB) features that will prevent undesired current flow from output to its input in either enabled or disabled state. The AOZ1377 can operate as either a sink or a source switch.

Enable

The EN pin is the ON/OFF control for the power switch. The device is enabled when EN pin is high and not in under-voltage lockout state. The EN pin must be driven to a logic high or logic low state to guarantee operation.

Alternatively, EN pin can be biased with resistor divider network from VIN so that device enable will be tracking the input voltage. While disabled, the AOZ1377DI draws 3μA from supply.

Input Under-Voltage Lockout (UVLO)

The under-voltage lockout (UVLO) circuit monitors the input voltage. The power switch is only allowed to turn on when input voltage is higher than UVLO threshold. Otherwise the switch is off.

Over-Voltage Protection (OVP)

The voltage at VIN pin is constantly monitored once the device is enabled. The over-voltage protection threshold can be programmed externally through POVP pin. In case input voltage exceeds the over-voltage protection threshold (V_{OVP}) of 1.2V at POVP pin, the power switch is either turned off immediately or kept off, depending on its initial state. FLTB will be pulled low to indicate a fault condition.

AOZ1377DI-01 can be restarted when voltage at POVP pin drops below the hysteresis voltage of V_{OVP_HYS} . Once the device detects that the OVP condition no longer exists, it will turn back on the switch after FLTB recovery time blanking (t_{REC}) (AOZ1377DI-01 only).

Programmable Current Limit and Over-Current Protection (OCP)

The AOZ1377DI implements current limit to ensure that the current through the switch does not exceed current limit threshold set by the external resistor R_{LIM} .

The current limit threshold can be estimated using the equations below:

$$I_{LIM} = \frac{1.24}{R_{LIM}} \times 5000 \times 3.14$$

where R_{LIM} unit is in ohm and I_{LIM} unit is in Ampere.

For example, a 3.92kΩ R_{LIM} resistor should be selected for 5A current limit threshold. Resistor with 1% tolerance is recommended for R_{LIM} .

AOZ1377DI continuously limits the output current when output is overloaded. The output current will be under regulation for 1ms (t_{OCP}). If the overloaded output condition still exists after this 1ms blanking time, the power switch will shut down. FLTB is also pulled low after a 1ms delay (t_{OCP_FLTB}) from over-current condition is detected.

For AOZ1377DI-01 Auto-Restart version, the power switch will be turn on again to restart after FLTB recovery time of 130ms (t_{REC}).

For AOZ1377DI-02 Latch Off version, the power switch will only be turned on after toggling the EN input logic to reset the device.

Severe overload causes power dissipation and die temperature to increase and may trigger thermal shutdown.

Short Circuit Protection (SCP)

When VOUT drops significantly lower than VIN, the system interprets that as a VOUT shorted to GND condition. From that point it takes 1μs ($t_{SC_RESPONSE}$) to respond and open the switch which will isolate VIN from VOUT.

For AOZ1377DI-01 Auto-Restart version, the power switch will be turn on again to restart after the recovery time of 130ms (t_{REC}).

For AOZ1377DI-02 Latch Off version, the power switch will only be turned on after toggling the EN input logic to reset the device.

Programming Soft Start

The AOZ1377DI start up delay and soft start time are programmable externally through SS pin. The turn-on time (0% to 100% of Vout) and turn-on delay time can be estimated using the equations below:

$$t_{ON} = \frac{C_{SS}}{2} \times \frac{VIN}{32}$$

$$t_{D_ON} = 0.35 + \frac{C_{SS}}{32}$$

where C_{SS} is in nF, VIN is in V, t_{ON} and t_{D_ON} are in ms.

For example, a 12V VIN and a 22nF soft start capacitor will generate soft start time (t_{ON}) of 4.125ms after start up delay (t_{D_ON}) of 1 ms. The calculated turn-on time is for 0% to 100% of Vout rising.

For prebias condition, part of the soft start time is incorporated in the delay time. The following equations can be used to estimate the soft start delay and ramp time for pre-bias condition:

$$t_{ON} = C_{SS} \times \frac{VIN - VOUT_{PREBIAS}}{32}$$

$$t_{D_ON} = 0.35 + \frac{C_{SS}}{32} + \frac{C_{SS} \times VOUT_{PREBIAS}}{32}$$

where C_{SS} is in nF, $VOUT_{PREBIAS}$ is the Output PreBias voltage in V, t_{ON} and t_{D_ON} are in ms.

The actual soft start time may not be equal to the estimated value from the above equation if the operating condition exceeds the SOA of the power switch.

True Reverse Current Blocking (TRCB)

True reverse-current blocking prevents undesired current flow from output to input when power switch is in either on or off state. When device is enabled, power switch is quickly turned off whenever output voltage is higher than input voltage by 30mV (V_{T_TRCB}). Once the device detects the TRCB condition it will quickly turn off the switch in 1μs (t_{TRCB}). FLTB will be pulled low to indicate a TRCB condition.

For AOZ1377DI-01 Auto-Restart version, the power switch can be turned on again when $VOUT < VIN$. Once the device detects that the TRCB condition no longer exists, it will turn back on the switch after FLTB recovery time blanking (t_{REC}).

Thermal Shut Down Protection (TSD)

Thermal shutdown protects device from excessive temperature. The power switch is turned off when the die temperature reaches thermal shutdown threshold of 140°C. There is a 30°C hysteresis. When thermal shutdown is activated, FLTB is pulled low immediately to report fault condition to host.

For AOZ1377DI-01 Auto-Restart version, the power switch will be turn on again after the temperature decreases by hysteresis of 30°C.

For AOZ1377DI-02 Latch Off version, the power switch stays off after thermal shutdown and can only turn on by either toggling EN or cycling the input power supply.

Startup

The device is enabled when $EN \geq V_{EN_H}$ and input voltage VIN is higher than UVLO threshold. The device first checks if any fault condition exists. If no fault exists, the power switch will turn on and the output starts to ramp up. Power switch is kept off if fault condition is detected.

Fault Reporting

AOZ1377DI protects itself and load from the following fault condition: over-voltage, over-current, short-circuit, reverse-current, and thermal shutdown.

The FLTB pin is an open drain output. It is asserted low when over-current, short-circuit, over-voltage, reverse current, or over-temperature condition occurs. The FLTB pin becomes high impedance when the fault conditions are removed. A pull-up resistor (R_{FLTB}) must be connected between FLTB to 5V to provide a logic signal.

When thermal shutdown is activated, FLTB is pulled low immediately to report fault condition to host.

For output current overload condition, FLTB pin is pulled low 1ms (t_{OCP_FLTB}) after device is in current limiting.

The power switch will be turned off and FLTB pin will be pulled low for a fault condition to indicate fault status of the device. The device will continuously monitor the fault conditions. See the Table 1 and 2 as a summary of the fault responses.

Table 1. AOZ1377DI-01 Fault flag response to all protection functions

Protection	Fault Response	FLTB Status
TRCB	Auto-restart	Low
SCP	Auto-restart	Low
Current Limit	Auto-restart	Low
TSD	Auto-restart	Low
OVP	Auto-restart	Low

Table 2. AOZ1377DI-02 Fault flag response to all protection functions

Protection	Fault Response	FLTB Status
TRCB	Latch-off	Low
SCP	Latch-off	Low
Current Limit	Latch-off	Low
TSD	Latch-off	Low
OVP	Latch-off	Low

For AOZ1377DI-01 (Auto-Restart version), the power switch is turned off under fault condition. The device will try to restart indefinitely for every t_{REC} of 130ms until it is disabled.

For AOZ1377DI-02 (Latch-Off version), the device will latch off under fault condition and can only turn on by either toggling EN or cycling the input power supply.

Input Capacitor Selection

The input capacitor prevents large voltage transients from appearing at the input, and provides the instantaneous

current needed each time the switch turns on to charge output capacitors and to limit input voltage drop. It also prevents high-frequency noise on the power line from passing through to the output. The input capacitor should be located as close to the pin as possible. A minimum of 10µF ceramic capacitor should be used. A higher capacitor value is strongly recommended to further reduce the transient voltage drop at the input.

Output Capacitor Selection

The output capacitor acts in a similar way. Also, the output capacitor has to supply enough current for a large load that it may encounter during system transient. This bulk capacitor must be large enough to supply fast transient load in order to prevent the output from dropping.

Power Dissipation Calculation

Calculate the power dissipation for normal load condition using the following equation:

$$\text{Power Dissipation} = R_{\text{ON}} \times (I_{\text{OUT}})^2$$

Layout Guidelines

Good PCB layout is important for improving the thermal and overall performance. To optimize the switch response time to output short-circuit conditions, keep all traces as short as possible to reduce the effect of unwanted parasitic inductance. Place the input and output bypass capacitors as close as possible to the VIN and VOUT pins. The input and output PCB traces should be as wide as possible for the given PCB space. Use a ground plane to enhance the power dissipation capability of the device.

For the most efficient heat sinking, connect as much copper as possible to the exposed pad. The exposed pad is the common drain of the power switch which must be electrically isolated.

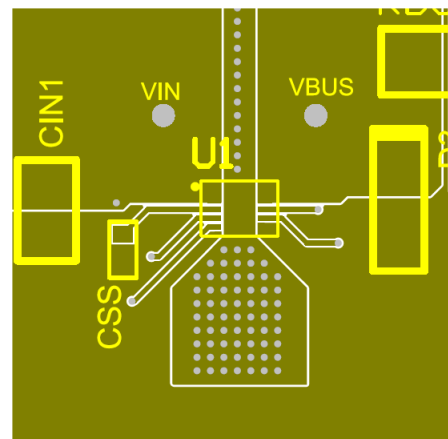


Figure 4. Top Layer

In addition to the top plane, if available, connect to the bottom layer plane for best thermal performance.

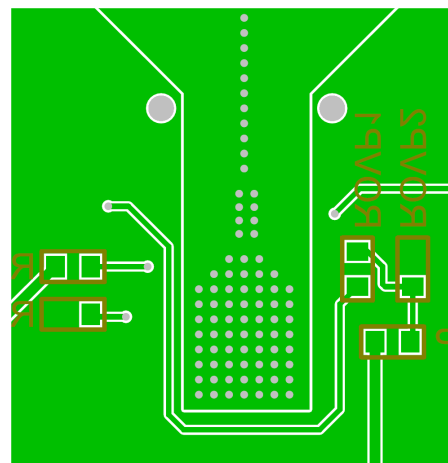
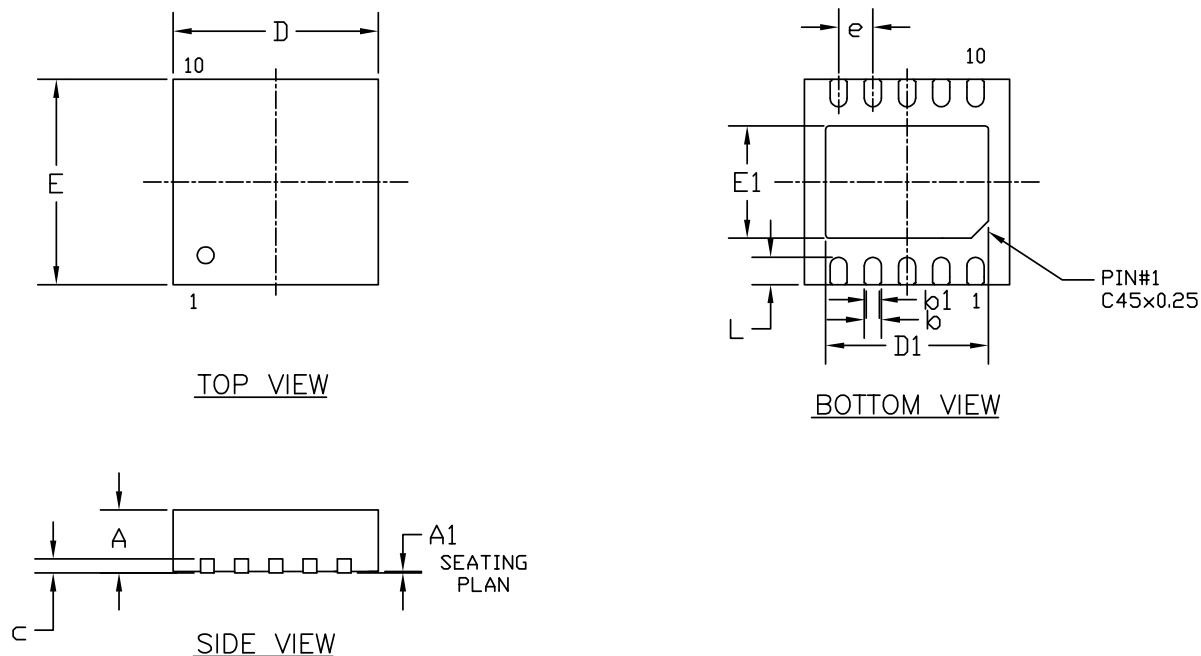
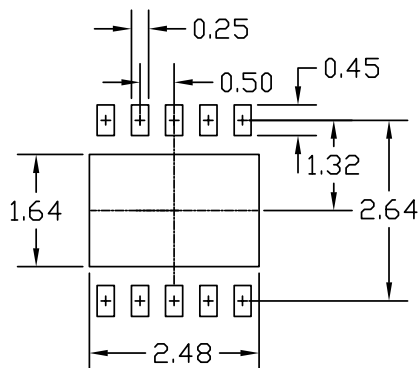


Figure 5. Bottom Layer

Package Dimensions, DFN3x3-10L



RECOMMENDED LAND PATTERN



UNIT: mm

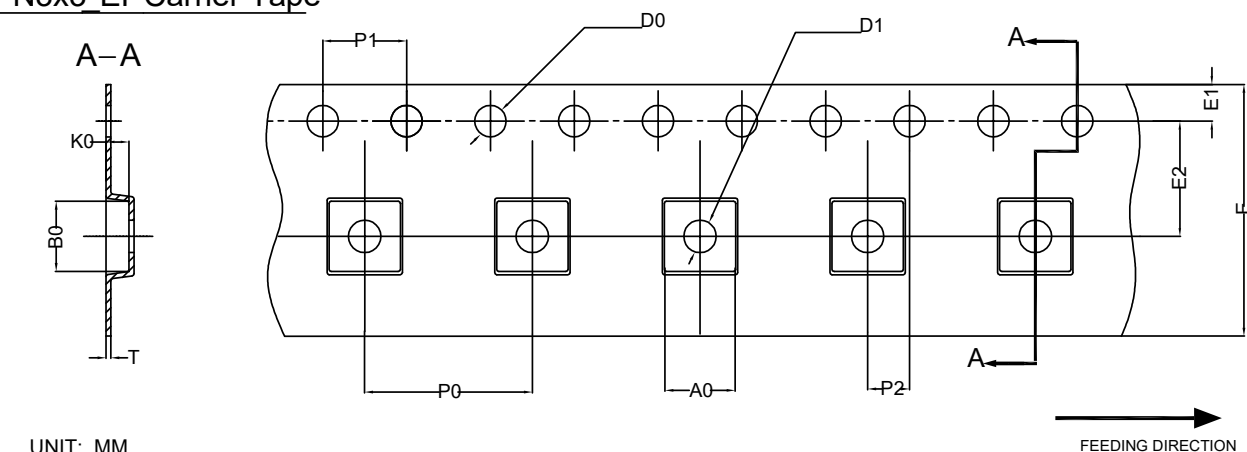
SYMBOLS	DIMENSION IN MM			DIMENSION IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.80	0.90	1.00	0.031	0.035	0.039
A1	0.00	0.02	0.05	0.000	0.001	0.002
b	0.20	0.25	0.30	0.008	0.010	0.012
b1	0.09	0.19	0.29	0.004	0.007	0.011
c	0.195	0.203	0.211	0.008	0.008	0.008
D	2.90	3.00	3.10	0.114	0.118	0.122
D1	2.28	2.38	2.48	0.090	0.094	0.098
E	2.90	3.00	3.10	0.114	0.118	0.122
E1	1.54	1.64	1.74	0.061	0.065	0.069
L	0.30	0.40	0.50	0.012	0.016	0.020
e	0.50BSC			0.020BSC		

NOTE

1. DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5M-1994.
2. CONTROLLING DIMENSION IS MILLIMETER.
CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.

Tape and Reel Dimensions, DFN3x3-10L

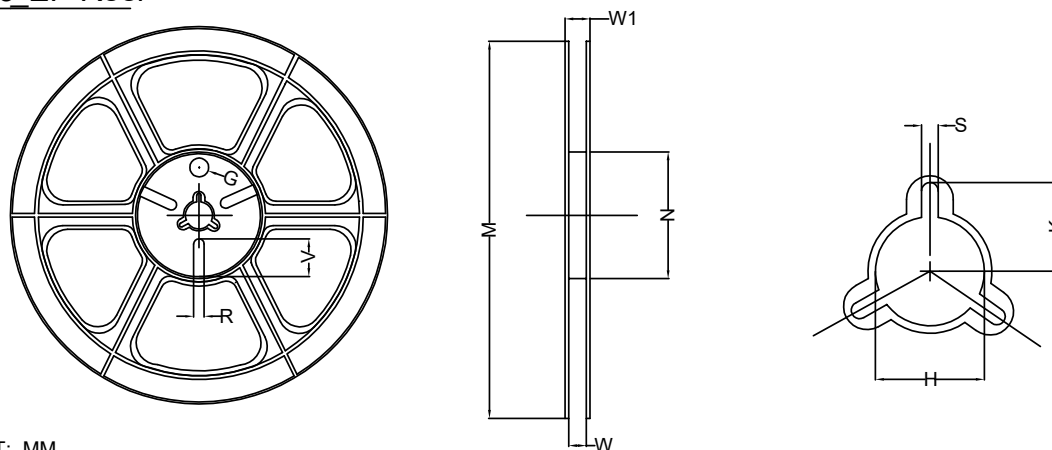
DFN3x3 EPCarrier Tape



UNIT: MM

PACKAGE	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
DFN3x3_EP	3.40 ±0.10	3.35 ±0.10	1.10 ±0.10	1.50 +0.10 -0	1.50 +0.10 -0	12.00 ±0.30	1.75 ±0.10	5.50 ±0.05	8.00 ±0.10	4.00 ±0.10	2.00 ±0.05	0.30 ±0.05

DFN3x3 EP Reel



UNIT: MM

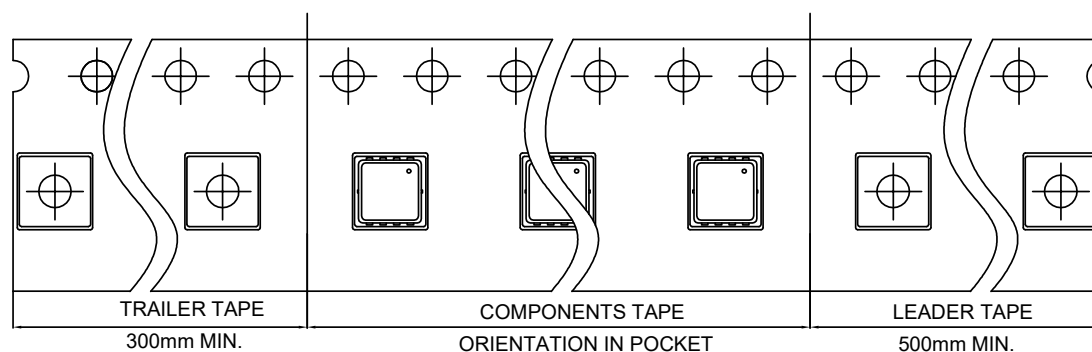
TAPESIZE	REEL SIZE	M	N	W	W1	H	K	S	G	R	V
12 mm	Ø330	Ø330.00 ±0.50	Ø97.00 ±0.10	13.00 ±0.30	17.40 ±1.00	Ø13.00 +0.50 -0.20	10.60	2.00 ±0.50	---	---	---

DFN3x3 EPTape

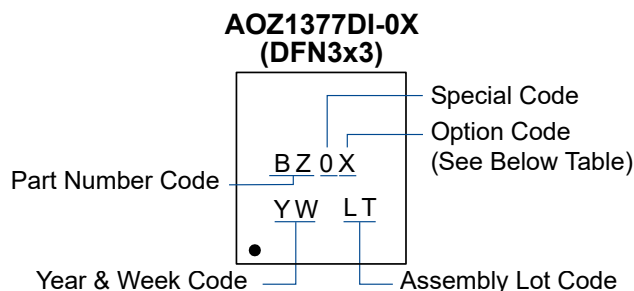
Leader / Trailer
& Orientation

PIC

Unit Per Reel:
5000pcs



Part Marking



Part Number	Fault Recovery	Code
AOZ1377DI-01	Auto-Restart	BZ01
AOZ1377DI-02	Latch-Off	BZ02

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.