

Evaluation Board User Guide

General Description

The AOZ2263AQI (-10, -15, -18) is a high-efficiency, easy-to-use DC/DC synchronous buck regulator that operates up to 28V. The device is capable of supplying 12A of continuous output current with an output voltage adjustable down to $0.8V \pm 1\%$.

The AOZ2263AQI Evaluation Board (EVB) proprietary constant on-time PWM control with input feed-forward results in ultra-fast transient response while maintaining relatively constant switching frequency over the entire input voltage range.

The AOZ2263AQI EVB features multiple protection functions such as VCC under-voltage lockout, cycle-by-cycle current limit, output over-voltage protection, short-circuit protection, and thermal shutdown.

The AOZ2263AQI EVB demonstrates the COT buck converter design.

Features include: wide input voltage range -4V to 28V; 12A continuous output current; output voltage adjustable down to $0.8\,V\,(\pm 1.0\%)$; low $R_{DS(ON)}$ internal NFETs with $14\,m\Omega$ high-side and $7\,m\Omega$ low-side; constant on-time with input feed-forward; programmable on-time up to $1.3\,\mu\text{s}/3.5\,\mu\text{s}$; selectable PFM light-load operation; ceramic capacitor stable; adjustable soft start; ripple reduction; power good output; integrated bootstrap diode; cycle-by-cycle current limit; short-circuit protection; over voltage protection; and thermal shutdown.

Applications include: portable computers; compact desktop PCs; servers; graphics cards; set-top boxes; LCD TVs; cable modems; point-of-load DC/DC converters; and telecom/networking/datacom equipment.

Evaluation Board Schematic

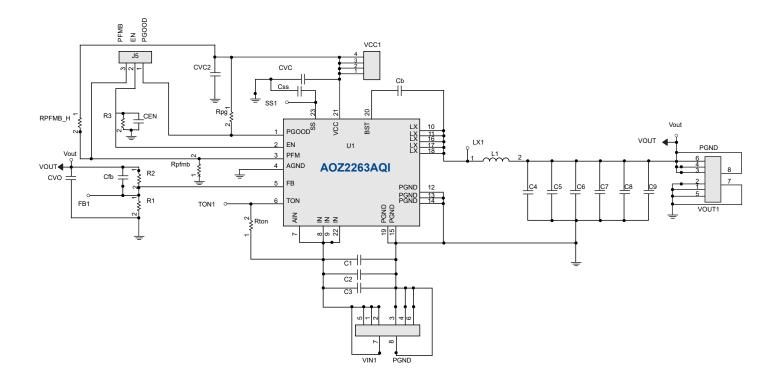




Table 1. AOZ2263AQI 12Vin to 1.8Vout Component List

Ref Designation	Part Number	Description		
СВ	GRM188R71H104KA01D	Cap, 100nF, 0603, 50V, X7R, 10%		
CEN	GRM188R71H102KA01D	Cap, 1nF, 0603, 50V, X7R, 10%		
СГВ	GRM188R71H101KA01D	Cap, 100pF, 0603, 50V, X7R, 10%		
CSS	GRM188R71H103KA01D	Cap, 10nF, 0603, 50V, X7R, 10%		
CVC	GRM188R61H475KALD	Cap, 4.7 µF, 0603, 50 V, X5R, 10%		
CVO, CVC2		Open		
C1,C8-C9		Open		
C2,C3	CL31A106KBHNNNE	Cap, 10µF, 1210, 50V, X5R, 10%		
C4-C7	CC5X226M8	Cap, 22µF, 0805, 25V, X5R, 10%		
L1	PI0040-1R5M	Inductor,1.5µH		
RPFMB, RPG, R3	100K	Res, 100kΩ, 0603, 1%, 1/10W		
RS, CS		Open		
Rton	180K	Res, 180K, 0603, 1%, 1/10W		
R1	10K	Res, 10K, 0603, 1%, 1/10W		
R2	12.7K	Res, 12.7K, 0603, 1%, 1/10W		
U1	AOZ2263AQI-XX	IC, QFN4X4		

Output voltage is set by R2: R2 = R1*(Vout-0.8)/0.8. Table 1 shows the value of the R2 typical output voltage.

Table 2. Option Table

Part Number	All Protection		Ripple Reduction		Max. On-Time		Package 4mmx4mm	
	Auto Restart	Latch	Yes	No	3.5 μ s	1.3 μ s	QFN-22L	QFN-23L
AOZ2263AQI-10		V	V			V	V	
AOZ2263AQI-15	V		V		V		V	
AOZ2263AQI-18		V	V			V		V

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PCB Layout

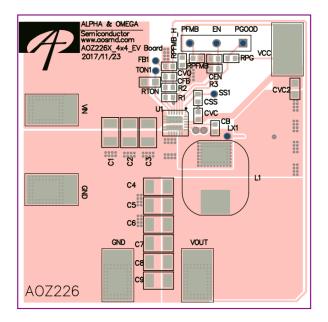


Figure 1. Top Layer

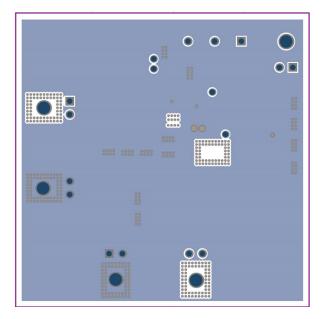


Figure 3. IN3-GND Layer

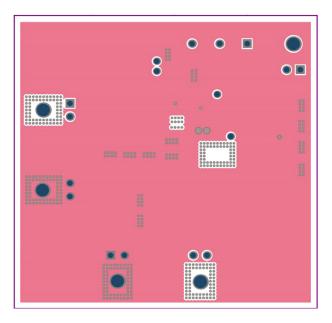


Figure 2. IN2-GND Layer

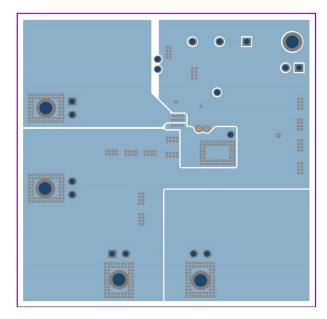


Figure 4. BOT Layer



Quick Start Guide

- 1. Connect the terminals of load to VOUT and GND connectors.
- 2. Connect the DC power supply to VIN and GND connects. Set the DC power supply voltage between the operating range of 4V and 28V.
- 3. Connect the DC power supply to VCC and GND connects. Set the DC power supply voltage between the operating range of 4.5V and 5.5V.
- 4. Connect the DC power supply to EN and GND connects. Set the DC power supply voltage between the operating range of 3.3V and 5.5V.
- 5. Measure input voltage at the Vin and GND connectors to eliminate the effect of voltage drop on wire between DC power supply and evaluation board.
- 6. Measure output voltage at the Vout and GND connectors to eliminate the effect of voltage drop on wire between load and evaluation board.
- 7. Use oscilloscope to monitor input ripple voltage across input capacitor C1.
- 8. Use oscilloscope to monitor output ripple voltage across output capacitor C7.
- 9. When monitoring the LX switching waveform, directly probe across the LX-PGND trace to minimize inductive ringing.

Note:

1. When testing the ripple voltage, remove the cap of the voltage probe and touch the probe tip directly across the Vin or Vout and GND terminals.

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