

A0Z71031QI

3 Rails 9+1+1 Hybrid Low P_Q Intel IMVP 9.1/9.2 Controller

General Description

The AOZ71031QI is a high performance digital and analog hybrid multiphase buck controller designed in compliance with Intel IMVP8, 9, and 9.1/9.2 platform specifications. It provides three output rails (up to 9+1+1) and supports 4 separate SVID domains. Up to 9 phases for core voltage domain (IA), 1 phase for graphics voltage domain (GT) and 1 phase for Auxiliary domain (SA) as well as the P_{SYS} domain's reporting functions, incorporated into a single SVID interface. AOS offers a novel AOS Advanced Transient Modulator (A²TM). It combines an advanced variable frequency hysteretic peak current mode control with proprietary phase current sensing scheme for fast transient response and low system cost. The control loop enhances light-load efficiency by seamlessly entering DCM mode of operation. Autonomous Phase Management also assures the optimized efficiency and power loss during light load with single phase DCM mode.

The AOZ71031QI is equipped with SMBus digital interface enabling register programming for tuning and configuration to minimize the system components and eliminate the need for manual solder rework on system board. Programmability can be done either by AOS GUI or customized ECS into the controller's built-in RAM or MTP. The controller provides MTP to store register settings once the configuration is finalized and the configuration can be updated more than ten times. In the production stage, the external resistor can be used for pin strap to choose 1 out of 6 config settings pre-programmed into the parts to achieve easy BOM management and minimize the number of PN and SKU.

Combined with AOS high performance DrMOS, the AOZ71031QI provides a complete power solution for Intel IMVP9.1/9.2 Meteor Lake S and Graphic card low power SKU applications. AOZ71031QI comes in a 7mm x 7mm 60-pin QFN package.

The AOZ71031QI controller features very low power consumption while still enabling digital interface control. This unique "Hybrid Digital" control scheme enables low quiescent power consumption in all power states as defined by the Intel IMVP9.1/9.2 platform to enable long system run times in battery life workloads.

The AOZ71031QI provides complete protection and warning functions including UVP, OVP, OCP, and OTP. Fault protection behavior can be easily programmed through SMBus. AOZ71031QI also offers real time telemetry information via SMBus for V_{IN} , V_{OUT} , temperature, output currents, power states, as well as PSYS/VSYS/IAUX pins reporting via SMBus.

Figure 1 shows the typical application implementation of the AOZ71031Ql device in a multiphase buck VR.

Features

- 5.2V to 24V V_{IN} input supply voltage
- Triple output rails: 9/8/7/6/5/4/3/2/1+1+1
- Meteor Lake S and Graphic Platform
- Support Discrete Inductor and 2-phase Coupled L
- Autonomous Phase Management including phase shedding and auto DCM to optimize power loss
- Digital & analog hybrid controller with SMBus programmability and lowest power consumption
- SVID Interface to CPU/GPU compliant with IMVP8, 9, 9.1 and the latest 9.2 specifications
 - Support Fast V-Mode (FVM) to protect CPU
- Differential remote sensing to achieve 0.5% regulated V_{OUT} accuracy
- Low quiescent current: 4mA at PS0 for 9+1 configuration
- Supports multi-sourced industry standard DrMOS or SPS power stages
- User friendly GUI for compensation and configurations with minimal external RC components.
- ECS programmability for configurations with Built-in MTP and RAM with more than 10 times configuration changes
- Pin Strap for easy configuration with 6 configuration setting with same PN to minimize number of SKU
- Proprietary, high performance Adaptive Transient On-Proprietary, high performance AOS Advanced Transient Modulator (A²TM) control scheme:
 - Variable frequency hysteretic peak current mode control gives fast transient response
 - Dynamic phase current balance
 - Excellent load-line control and phase current sensing
 - Seamless CCM to DCM control to maximize efficiency
- System Input Power Monitoring (both P_{SYS} and V_{SYS})
- 300 kHz to 1.8 MHz programmable switching frequency
- Acoustic Noise Suppression
- Output Under-Voltage Protection (UVP)
- Output Over-Voltage Protection (OVP)
- Over-Current Protection (OCP)
- Over-Temperature Protection (OTP)
- QFN7x7-60L package

Applications

- Desktop computers
- Intel graphic cards





Typical Application

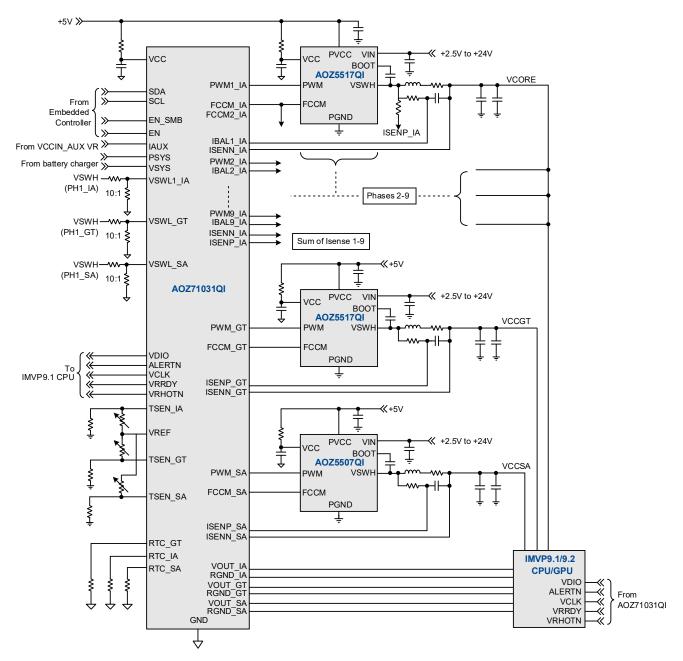


Figure 1. AOZ71031QI Typical Application Diagram



Ordering Information

| Part Number ⁽¹⁾ | Junction Temperature Range | Package | Environmental | |
|-------------------------------|----------------------------|------------|---------------|--|
| AOZ71031QI-xxx ⁽²⁾ | -40°C to +125°C | QFN7x7-60L | RoHS | |

Notes:

- 1. For each customer, the full PN already created for order is on the last page of this DS. Please refer to last page for more information.
- 2. "xxx" is the configuration code identifier (also called sub-part number) for the register settings stored in the internal non-volatile memory (NVM). Each "x" can be a value between 0 and 9 and A-Z (except I, J, O, Q). Please work with an AOS Sales/FAE to create this unique number. Each project or board might need to use different sub-PN as the register setting might be different.



AOS products are offered in packages with Pb-free plating and compliant to RoHS standards.

Please visit https://aosmd.com/sites/default/files/media/AOSGreenPolicy.pdf for additional information.

Pin Configuration

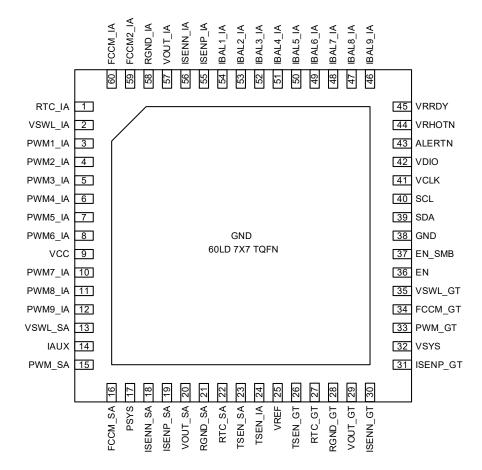
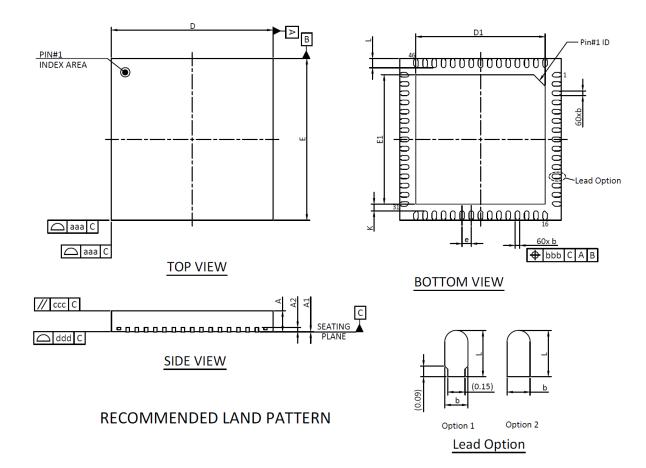


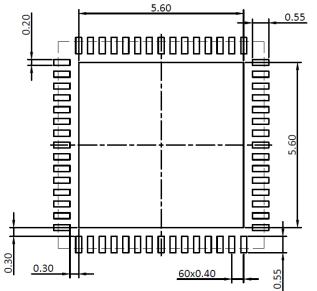
Figure 2. Pin Definition QFN7x7-60L (Top View)

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Package Dimensions, QFN7x7-60L





| SYMBOLS | DIMENSION IN MM | | | DIMENSION IN INCHES | | |
|-----------|-----------------|------|-------|---------------------|-------|-------|
| SYIVIBULS | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | 0.80 | 0.90 | 1.00 | 0.031 | 0.035 | 0.039 |
| A1 | 0.00 | 0.02 | 0.05 | 0.000 | 0.001 | 0.002 |
| A2 | 0.15 | 0.20 | 0.25 | 0.006 | 0.008 | 0.010 |
| b | 0.15 | 0.20 | 0.25 | 0.006 | 0.008 | 0.010 |
| D | 7.00 BSC | | | 0.197 BSC | | |
| D1 | 5.45 | 5.60 | 5.70 | 0.215 | 0.220 | 0.224 |
| Е | 7.00 BSC | | | 0.197 BSC | | |
| E1 | 5.45 | 5.60 | 5.70 | 0.215 | 0.220 | 0.224 |
| е | 0.40 BSC | | | 0.016 BSC | | |
| K | 0.20 | 0.30 | 0.40 | 0.008 | 0.012 | 0.016 |
| L | 0.30 | 0.40 | 0.50 | 0.012 | 0.016 | 0.020 |
| aaa | 0.10 | | | 0.004 | | |
| bbb | 0.07 | | | 0.003 | | |
| ccc | 0.10 | | | 0.004 | | |
| ddd 0.08 | | | 0.003 | | | |

UNIT: mm

NOTE:

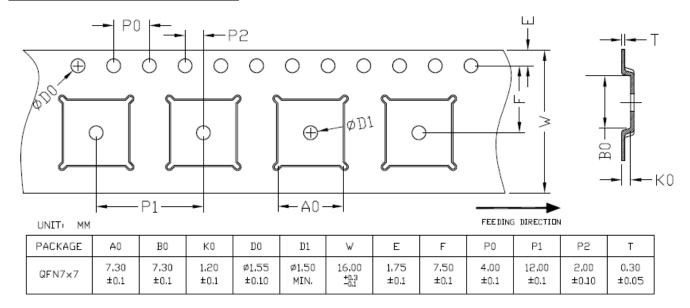
- 1. CONTROLLING DIMENSION IS MILLIMETER.
- 2. CO-PLANARITY APPLIES TO THE EXPOSED PAD AND THE LEADS.

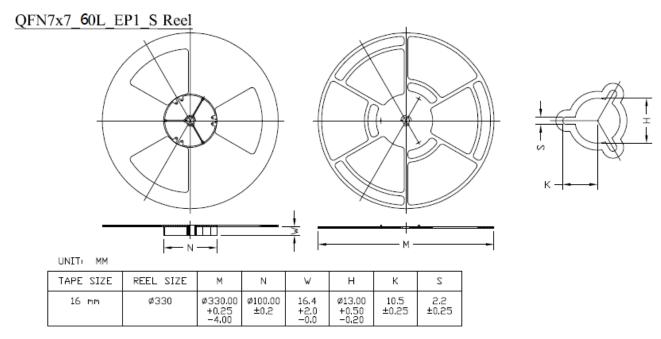
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Tape and Reel Dimensions, QFN7x7-60L

QFN7x7_60L_EP1_S Carrier Tape

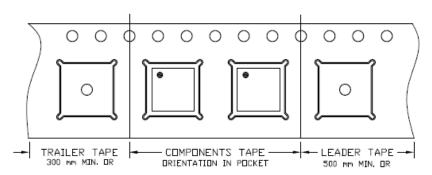




QFN7x7 60L EP1 S Tape

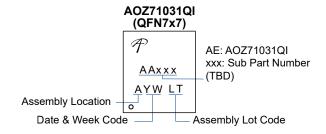
Leader / Trailer & Orientation

Unit Per Reel: 3000pcs





Part Marking



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